



Global Communication Semiconductors, LLC

Corporate & Foundry

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Website: www.gcsincorp.com

Process Integration Engineer - Opto

Job Req: #0772
Department: Opto – R&D
Status: Full-Time/Salaried
Shift: Day
Hiring Manager: Yeh Tsai
Salary Range: \$70,000 - \$115,000

Job Description

- Experience and knowledge in semiconductor wafer process for OPTO components
- Participate in process development and improvement efforts in different areas in the GCS compound semiconductor FAB
- Serves as project engineer to oversee FAB process lots for developments as well as contract deliverables: from mask layout review and lot starts to wafers and test data delivery
- Collects and analyzes device test results
- Conducts review and provides timely reports to Managers
- Trains techs and operators as assigned

Job Requirements

- B.S., M.S. in engineering (EE, CE, MS) or related field
- Must have clean room and hands-on wafer processing experience
- Experience in automated optical inspection and wafer scribe/break or sawing process preferred
- Excel and document drafting skills
- Ability to effectively work in a fast-paced environment, with shifting priorities, to support overall business objectives
- Ability to effectively communicate and interact with all other employees at all levels, vendors, customers, and management in a professional manner